

Europäisches Patentamt

European Patent Office

Office européen des brevets



11 Publication number:

0 656 628 A3

12

EUROPEAN PATENT APPLICATION

21 Application number: 94308288.3

(9) Int. Cl.6: **G11C** 16/06, G05F 3/24

② Date of filing: 10.11.94

Priority: 01.12.93 US 160582

Date of publication of application: 07.06.95 Bulletin 95/23

Designated Contracting States:

BE DE DK ES FR GB GR IE IT LU NL PT SE

Date of deferred publication of the search report:
 09.08.95 Bulletin 95/32

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9 Programmed reference.

(57) A programmable reference used to identify a state of an array cell in a multi-density or low voltage supply flash EEPROM memory array. The programmable reference includes one or more reference cells, each reference cell having a floating gate which is programmed to control its threshold value. The array cells are read by applying an identical voltage to the gate of the array cell and the reference cell and comparing outputs to determine the array cell state. During read of an array cell, the programmable reference cell is biased the same as the array cell, so that the difference in threshold values between reference cells and array cells remain constant with a change in Vcc. Circuitry is included for programming the reference cells utilizing a simple resistor ratio. Programming is performed at test time, preferably by the manufacturer, to assure Vcc remains within strict tolerances. The array cells are programmed and read without resistor biasing and under looser tolerances using the reference cells at a later time.

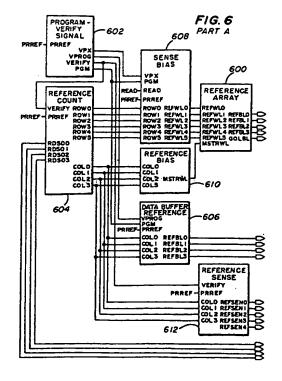
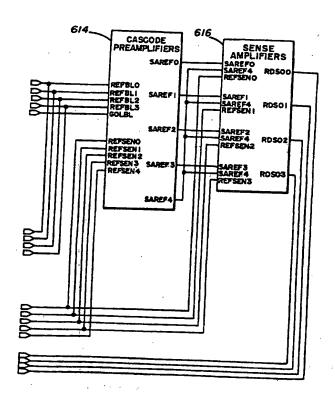


FIG. 6





EUROPEAN SEARCH REPORT

Application Number EP 94 30 8288

Category	of relevant p	indication, where appropriate, assages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.CL6)
X	WO-A-90 12400 (SUN		1-10, 17-19, 21-25	G11C16/06 G05F3/24 G11C11/56
A	* the whole docume	nt *	11-16,20	
X -	EP-A-O 409 697 (GEINTERNATIONAL)		1,3-6	
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				TECHNICAL FIELDS SEARCHED (Int.Cl.6)
				G11C
	The present search report has l	ocen drawn up for all claims		
Place of search THE HAGUE		Date of completion of the search 14 June 1995	Deg	Examinar raeve, L
X : part Y : part doc:	CATEGORY OF CITED DOCUME icularly relevant if taken alone icularly relevant if combined with an insent of the same category inological background	NTS T: theory or pris	ocipie underlying the document, but public g date ed in the application of for other reasons	levention